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... with saving and restoring the **thread** data structures ... part, containing the main TCP **state machine**, is located ... If the **embedded application** needs to become a web ...

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**Embedded Systems -> VLIW chip complicates pSOS porting**

... files and multi-issue slots, and a complex **state machine**. ... calls that can be used by an **embedded application**. ... Each task is a **thread** of independent actions that ...

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**Teja Corporate | Technology FAQs**

... more complexity, development time and cost into the **embedded application**. ... as well as the **state machine** diagram from ... closely related to the **thread** and memory ...

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... Using the microengine **thread** contexts, each microengine ... The hardware transmit **state machine** takes care of ... inevitable shift in **embedded application** development. ...

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... and debug than the **state-machine** paradigm ... cooperative\* multithreading into an **embedded application**, providing you ... going to write assembler **thread**-switching code ...

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... One example of an **embedded application** subsystem with ... a single **thread** (the primary **thread**) to perform the ... function implements a finite **state machine** to monitor ...

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... For an **embedded application** it is like driving a ... the socket, the client handling **thread** closes its ... described very succinctly as a **state machine**: **Dev Articles 06** ...

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**EDN - Pick and place: Linux grabs the embedded market**

... required an RTOS to meet **thread**-scheduling demands may ... environment to support an **embedded application** using free ... it/etlinux, FSMLabs (Finite **State Machine** Labs) 1 ...

[www.reed-electronics.com/ednmag/article/CA253780?pubdate=10%2F31%2F2002](http://www.reed-electronics.com/ednmag/article/CA253780?pubdate=10%2F31%2F2002) - [Similar pages](#)

Scalable Intel® IXA and its Building Blocks for Networking ...

... the microengines are running an **embedded application**, they don ... relinquish control and allow another **thread** to run. ... act as a programmable **state machine** to manage ...

[www.intel.com/technology/itj/2003/volume07issue04/art09\\_ixa/p06\\_ixp\\_npu.htm](http://www.intel.com/technology/itj/2003/volume07issue04/art09_ixa/p06_ixp_npu.htm) - 67k - [Cached](#) - [Similar pages](#)

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